

Exhibit 33

UNITED STATES DISTRICT COURT
FOR THE EASTERN DISTRICT OF TEXAS
MARSHALL DIVISION

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NETLIST, INC.,

Plaintiff,

vs.

No. 2:22-cv-203-JRG-RSP

MICRON TECHNOLOGY, INC.;

MICRON SEMICONDUCTOR

PRODUCTS, INC.; MICRON

TECHNOLOGY TEXAS LLC,

Defendants.

_____/

REMOTE VIDEOTAPED DEPOSITION OF
BOE HOLBROOK
30(B)(6) DESIGNEE, MICRON

WEDNESDAY, AUGUST 30, 2023

REPORTED BY: HOLLY THUMAN, CSR No. 6834, RMR, CRR
JOB NUMBER 6066631

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Remote videotaped deposition of BOE HOLBROOK,
taken by the Plaintiff, with the witness located in
Boise, Idaho, commencing at 10:16 A.M. Mountain Time,
on WEDNESDAY, AUGUST 30, 2023, before me, HOLLY THUMAN,
CSR, RMR, CRR.

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(ALL APPEARANCES REMOTE)

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ALSO PRESENT:

TONY NOKES, Videographer
CARRISA NARCISO, Tech Concierge
BECKY CARRIZOSA, In-house counsel, Micron

I N D E X

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EXHIBITS MARKED FOR IDENTIFICATION

NO.	DESCRIPTION	PAGE
Exhibit 1	JEDEC Standard JESD82-31A, August 2019 (SAM-NET00332467 through -665)	16
Exhibit 2	JEDEC Standard JESD82-32A, August 2019 (MICNL203-00012556 through -755)	17
Exhibit 3	Micron document, "3-Dimensional Stack (3DS) DDR4 SDRAM"	37
Exhibit 5	Micron document "DDR4 SDRAM LRDIMM" (MICNL203-00001364 through -391)	55
Exhibit 6	Micron document, "DDR5 SDRAM RDIMM Addendum" (MICNL203-00000678 through -685)	91
Exhibit 7	Master Revision Control Document 3408.01	90
Exhibit 8	Micron document, "DDR5 SDRAM SODIMM Core" (MICNL203-00000562 through -580)	90
Exhibit 9	Master Revision Control Document 3163.01	90
Exhibit 10	Master Revision Control Document 3405.1	92

(Cont'd)

1	(Exhibits, cont'd)	
2	Exhibit 12	Press release, May 7, 2012, "Micron Announces Its First Fully Functional DDR4 DRAM Module" 110
3		
4	Exhibit 14	Presentation deck, "Micron/Netlist Partnership Opportunity, April 2015" 112
5		(NL-MIC-203_00041942 through -2004)
6	Exhibit 16	Micron document, "DDR4 SDRAM LRDIMM" 99
7		(MICNL203-00001364 through -391)
8	Exhibit 17	US Patent 10,949,339, Lee et al. 105
9	Exhibit 18	Page from presentation deck with diagrams headed "Micron" Nd "Netlist Patents" 108
10		
11	Exhibit 19	Texas Instruments presentation deck, "Alternative HDLR Dimm Concept," 124
12		October 6, 2008
13		(NETLIST_SAMSUNG_EDTX00056236 through -245)
14	Exhibit 20	Document marked as Exhibit 19, with highlights 126
15	Exhibit 21	Page from presentation deck with headings for Micron, Netlist Patents, and TI JEDEC Presentation 133
16		
17	Exhibit 23	Presentation deck, "Micron/Netlist Partnership Opportunity, April 2015" 142
18		(NL-MIC-203_00041942 through -2004)
19	Exhibit 25	Minutes of Meeting No. 164, JC-40 Digital Logic Committee, December 9, 2010 (SAM-NET00078119 through -134) 144
20		
21	Exhibit 26	Richtek document, "DDR5 VR on DIMM PMIC" (MICNL203-00059886 through -60009) 148
22		
23	Exhibit 27	Minutes of Meeting No. 193, JC-40 Digital Logic Committee, June 4-6, 2018 (SAM-NET00246201 through -225) 156
24		
25	(Cont'd)	

1 (Exhibits, cont'd)

2 Exhibit 29 Presentation slide, "4.3.1.0, 158
3 #2259.39, Micron, Voltage Regulators
4 for DDR5 DIMMs"

5 Exhibit 30 Minutes of Meeting No. 59, JC-45 162
6 Module Committee, June 4-6, 2018
7 (NETLIST_SAMSUNG_EDTX00158253 through
8 -287)

9 --o0o--

10 INSTRUCTIONS TO WITNESS/REQUESTS TO MARK TRANSCRIPT

11		PAGE	LINE
12	Instruction not to answer	76	23
13	Instruction not to answer	175	15
14	Instruction not to answer	175	24

15 --o0o--

16 REPORTER'S NOTE: Exhibits Numbers 4, 11, 15, 22, 24,
17 and 28 were not used.

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23	Q. I've marked as Exhibit 1 what is the	10:34:46
24	DDR -- DDR4RCD.	10:34:49
25	Do you see that?	10:34:51

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Calendar-CA@veritext.com 866-299-5127

1 Q. By the way, just so -- before we do that, 11:08:23

2 Exhibit 1 is the RCD specification. 11:08:26

3 Do you see that? 11:08:29

4 A. Yes, I see that. 11:08:31

█ █ [REDACTED] █

█ [REDACTED] [REDACTED] █

█ █ [REDACTED] █

█ [REDACTED] █

█ [REDACTED] █

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█ [REDACTED] █

█ [REDACTED] █

█ [REDACTED] █

█ [REDACTED] █

█ [REDACTED] █

20	Q. And "CS" stands for chip-select. Is that	11:42:56
21	correct?	11:43:01
22	A. That's correct.	11:43:02
23	Q. And in JEDEC DDR parlance, chip-selects	11:43:03
24	select a rank. Is that correct?	11:43:07
25	A. Yes. That's correct.	11:43:13

■ [REDACTED]

■ [REDACTED]

■ [REDACTED]

■ [REDACTED]

■ [REDACTED]

■ [REDACTED]

■ [REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

7 Q. And, by the way, do you know what the 11:46:45

8 difference is -- between a signal and an encoded 11:46:47

9 packet of data is in JEDEC? 11:46:50

10 A. JEDEC definition, I do not know what that 11:46:57

11 would be; but I understand what a signal would be 11:47:00

12 versus an encoded packet of data. 11:47:03

13 Q. What's the difference between a signal and 11:47:07

14 an encoded packet of data? 11:47:10

15 A. So a signal, from my understanding, is a 11:47:12

16 single, defined signal, a 1 or a 0. Encoded data, 11:47:16

17 group of data, would be a group of 1s and 0s that 11:47:23

18 would be encoded by a device. 11:47:28

19 Q. Do you remember FBDIMMs? 11:47:32

20 A. I am familiar with FBDIMMs. 11:47:36

21 Q. A little before your time? 11:47:39

22 A. A little bit. 11:47:42

23 Q. And you know that they use an advanced 11:47:43

24 memory buffer. 11:47:45

25 Do you remember that? 11:47:47

1 A. Yes, sir. 11:47:48

2 Q. And in an advanced memory buffer, they 11:47:48

3 use -- that uses encoded data as opposed to data 11:47:52

4 signals. Correct? 11:47:55

5 A. I wouldn't know. AMB and FBDIMM was not 11:48:00

6 my area of expertise. 11:48:04

7 Q. But you understand that there is a 11:48:06

8 difference between encoded data and data signals in 11:48:08

9 terms of how you're controlling memory devices on a 11:48:12

10 module. 11:48:15

11 A. So how memory devices are controlled on a 11:48:18

12 module in respect to FBDIMM, yes, I understand 11:48:21

13 that. 11:48:28

█ █ [REDACTED] █

█ [REDACTED] █

█ [REDACTED] █

█ [REDACTED] █

█ [REDACTED] █

█ [REDACTED] █

█ █ [REDACTED] █

█ [REDACTED] █

█ [REDACTED] █

█ [REDACTED] █

█ █ [REDACTED] █

█ [REDACTED] █

CERTIFICATE OF REPORTER

I, HOLLY THUMAN, a Certified Shorthand Reporter, hereby certify that the witness in the foregoing deposition was by me duly sworn to tell the truth, the whole truth, and nothing but the truth in the within-entitled cause; that said deposition was taken down in shorthand by me, a disinterested person, at the time and place therein stated; and that the testimony of said witness was thereafter reduced to typewriting by computer, to the best of my ability via remote videoconferencing, under my direction and supervision;

That before completion of the deposition review of the transcript [] was [X] was not requested/offered. If requested, any changes made by the deponent (and provided to the reporter) during the period allowed are appended hereto.

I further certify that I am not of counsel or attorney for either or any of the parties to the said deposition, nor in any way interested in the event of this cause, and that I am not related to any of the parties thereto.

DATED: 9/05/2023



HOLLY THUMAN, CSR

JASON G. SHEASBY, ESQ.

JSheasby@irell.com

September 5, 2023

RE: NETLIST, INC. vs. MICRON TECHNOLOGY, INC.

AUGUST 30, 2023-BOE HOLBROOK-30(B)(6)-JOB NO.6066631

The above-referenced transcript has been completed by Veritext Legal Solutions and review of the transcript is being handled as follows:

___ Per CA State Code (CCP 2025.520 (a)-(e)) - Contact Veritext to schedule a time to review the original transcript at a Veritext office.

___ Per CA State Code (CCP 2025.520 (a)-(e)) - Locked .PDF Transcript - The witness should review the transcript and make any necessary corrections on the errata pages included below, notating the page and line number of the corrections. The witness should then sign and date the errata and penalty of perjury pages and return the completed pages to all appearing counsel within the period of time determined at the deposition or provided by the Code of Civil Procedure.

___ Waiving the CA Code of Civil Procedure per Stipulation of Counsel - Original transcript to be released for signature as determined at the deposition.

___ Signature Waived - Reading & Signature was waived at the time of the deposition.

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1 ___ Federal R&S Requested (FRCP 30(e)(1)(B)) - Locked .PDF
2 Transcript - The witness should review the transcript and
3 make any necessary corrections on the errata pages included
4 below, notating the page and line number of the corrections.
5 The witness should then sign and date the errata and penalty
6 of perjury pages and return the completed pages to all
7 appearing counsel within the period of time determined at
8 the deposition or provided by the Federal Rules.
9 _X_ Federal R&S Not Requested - Reading & Signature was not
10 requested before the completion of the deposition.